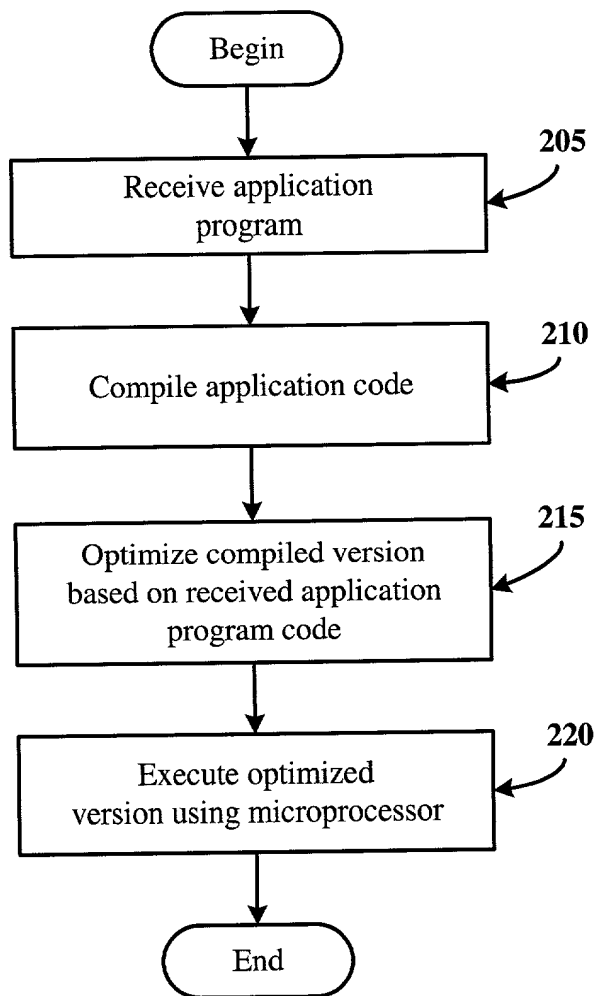


Figure 1

Conventional Profile Guided Optimization



**Figure 2**

Conventional Optimization  
Without Collection of Profile Data

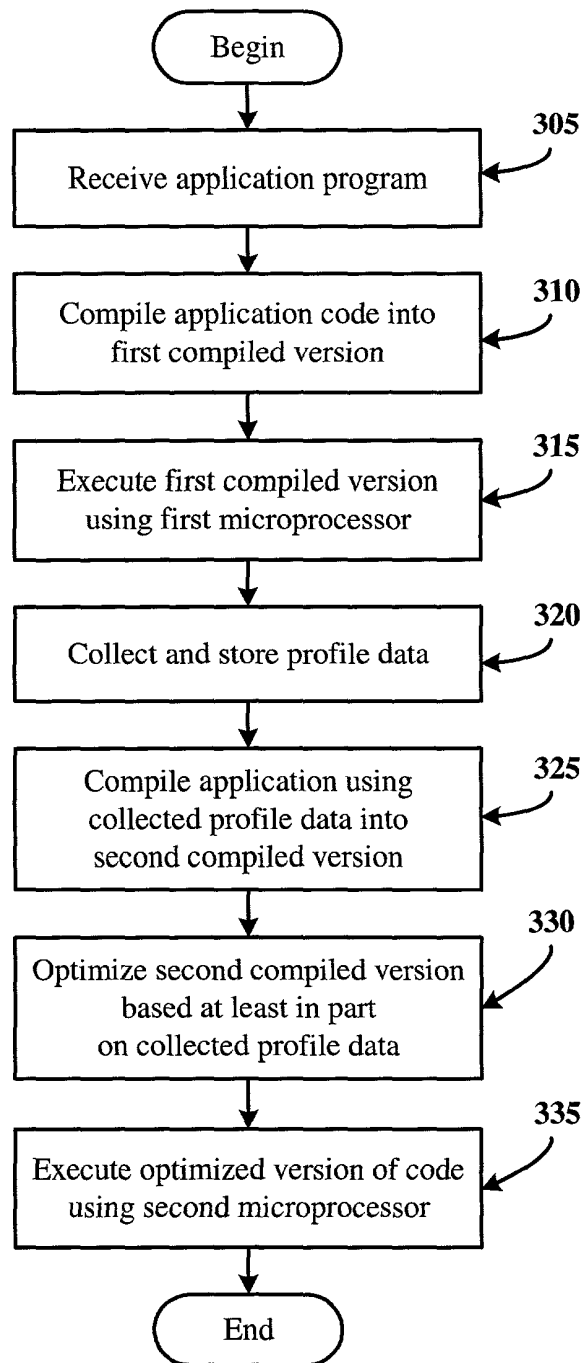


Figure 3

Cross Profile Guided  
Optimization

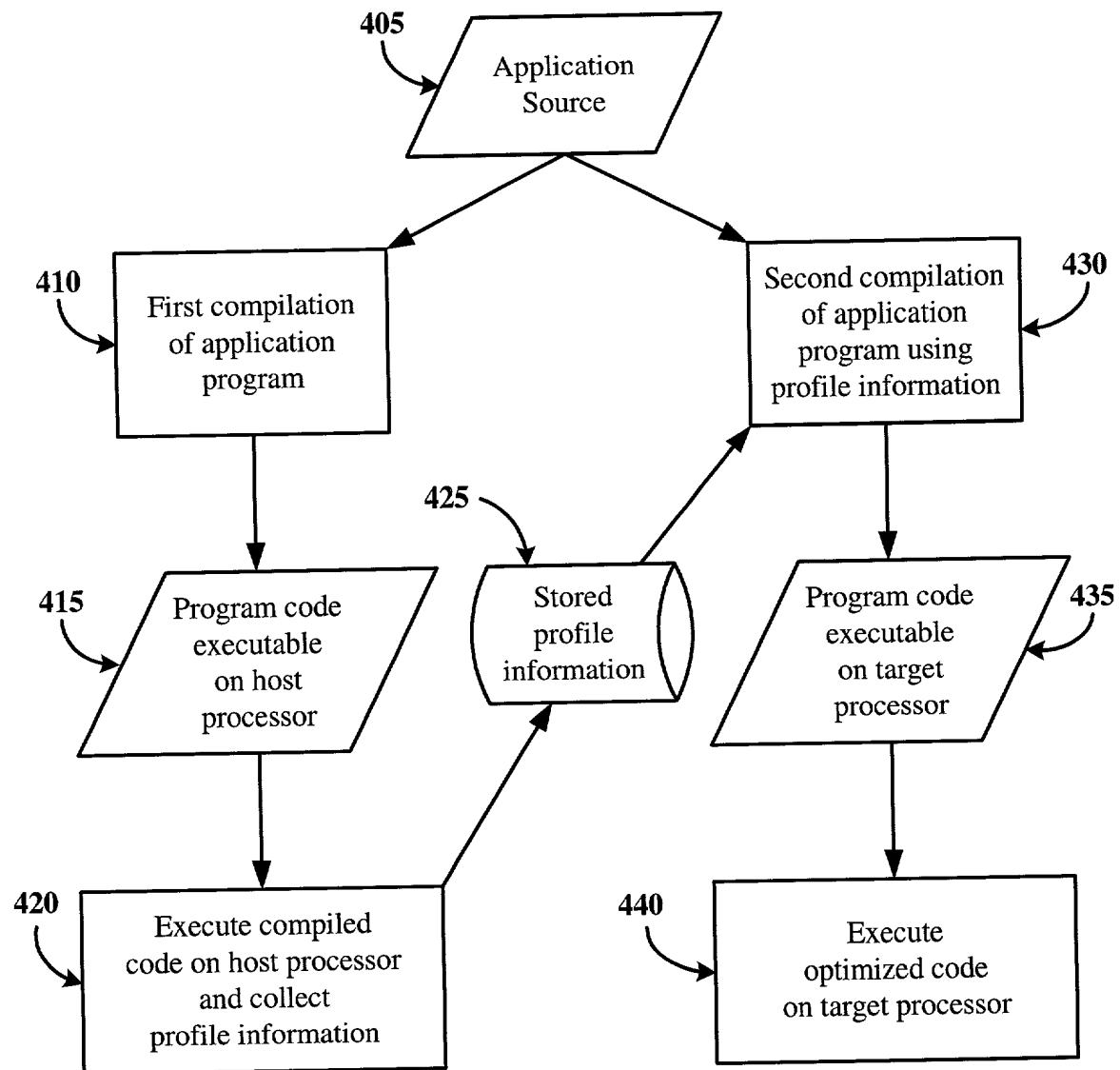


Figure 4

Exemplary Cross Profile Optimization Process

FIG. 5 is a block diagram of an exemplary device 505 subject to optimization. The device 505 includes an application program 525, a first compiler 530, a second compiler 535, profile data 540, a first compiled version 545, and a second compiled version 550. The application program 525 is compiled by the first compiler 530 to produce the first compiled version 545. The first compiled version 545 is then compiled by the second compiler 535, using the profile data 540, to produce the second compiled version 550. The device 505 also includes a host processor 510 and an embedded processor 515. The first compiled version 545 is executed on the host processor 510, and the second compiled version 550 is executed on the embedded processor 515.

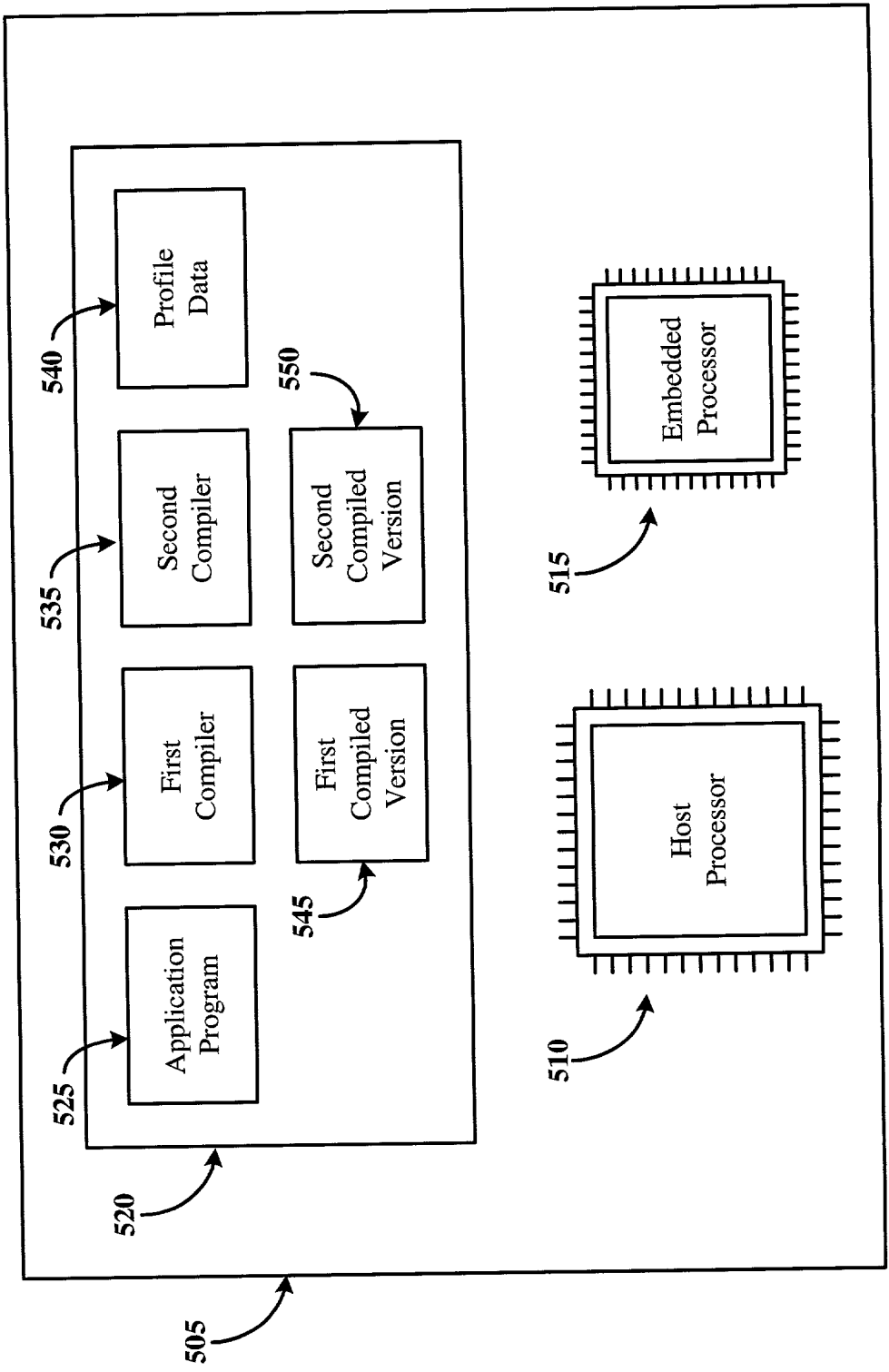


Figure 5

Exemplary Device  
Subject to Optimization